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<p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p><i>(use as many sheets as necessary)</i></p>			Complete If Known	
			Application Number	09/735,899
			Filing Date	December 14, 2000
			First Named Inventor	JOURDAN et al
			Group Art Unit	2183
			Examiner Name	B.J. O'Brien
			Attorney Docket Number	2207/9807
Sheet	1	of	2	

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FOREIGN PATENT DOCUMENTS						
Examiner Initials ^a	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ^a
		Country Code ² - Number ³ - Kind Code ⁴ (if known)				

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
AL		Jourdan et al, "eXtended Block Cache", Proceedings of Int'l. Symposium on High Performance Computer Architecture, pp 1-10, January 2000	
AL		Solomon et al, "Micro-Operation Cache: A Power Aware Frontend for Variable Instruction Length ISA", ISLPED '01. August 6-7, 2001	
AL		Bellas et al, "Architectural and Compiler Techniques for Energy Reduction in High Performance Microprocessors", <i>IEEE Transactions on VLSI</i> , Vol. 8, No. 3, June 2000	

Examiner Signature	<i>Amee Li</i>	Date Considered	<i>8-30-2005</i>
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No. 1	U.S. Patent Document Number Kind Code 2 (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Examiner Initials *	Cite No. 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T 2
AL		Friendly et al, "Alternative Fetch and Issue Policies for the Trace Cache Fetch Mechanism", 30th Annual IEEE/ACM Intl. Symposium on Microarchitecture, Research Triangle Park, North Carolina, December 1-3, 1997			
AL		Intrater et al, "Performance Evaluation of a Decoded Instruction Cache for Variable Instruction-Length Computers", 19th Annual Intl. Symposium on Computer Architecture, Gold Coast, Australia, May 19-21, 1992			
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AL		Glaskowsky, Peter N., "Pentium 4 (Partially) Previewed", <i>Microprocessor Report</i> , Vol. 14, Archive 8, pp. 1, 11-13, August 2000			
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AL		Upton, Michael, "The Intel Pentium® 4 Processor", http://www.intel.com/pentium4 , October 2000			
AL		Rotenberg et al, "Trace Cache: A Low Latency Approach to High Bandwidth Instruction Fetching", Proceedings, 29th Annual IEEE/ACM Intl. Symposium on Microarchitecture, MICRO-29, IEEE Computer Society Tech. Comm. on Microprogramming and Microarchitecture, Assn. for Computing Machinery SIGMICRO, Paris, France, December 2-4, 1996			
Examiner Signature	since li			Date Considered	8-29-2005

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